What Is Claimed Is:

- 1. A phase-locked loop circuit for reproducing a channel clock in synchronism with data read from a disk-shaped recording medium driven for rotation, said phase-locked loop circuit comprising:
- a frequency dividing means inserted in a desired signal path within said phase-locked loop circuit for dividing frequency of an input signal; and
- a frequency dividing ratio switching means capable of selecting a predetermined frequency dividing ratio set for each of signal formats reproduced from disk-shaped recording media of at least two different standards.
- 2. A phase-locked loop circuit as claimed in claim

 1, wherein said predetermined frequency dividing ratio is

 set on the basis of a relation between a reference clock

 frequency and a channel clock frequency corresponding to

 each of the signal formats.

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